

18-550 HW#6 Solutions

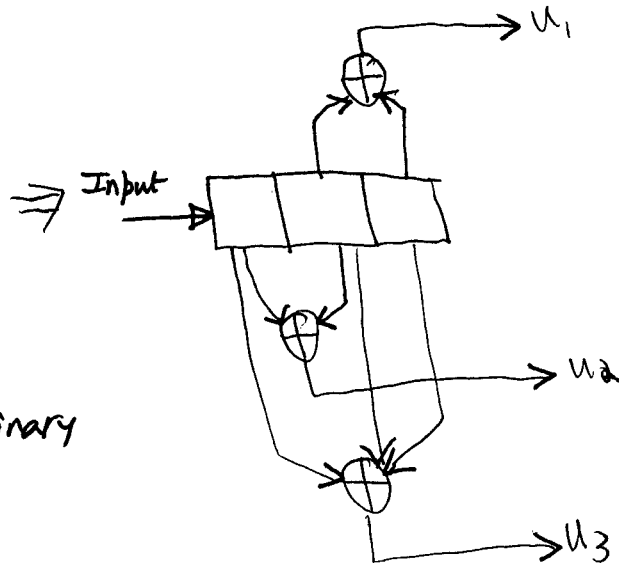
Problem 1 (6.1 in Sklar)

$$g_1(x) = x + x^2$$

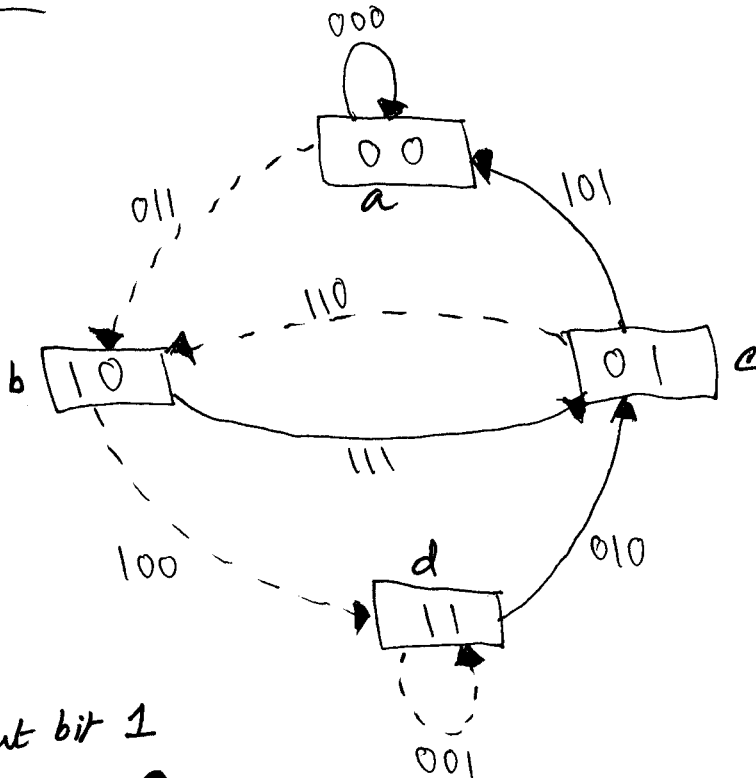
$$g_2(x) = 1 + x$$

$$g_3(x) = 1 + x + x^2$$

All addends are binary addends



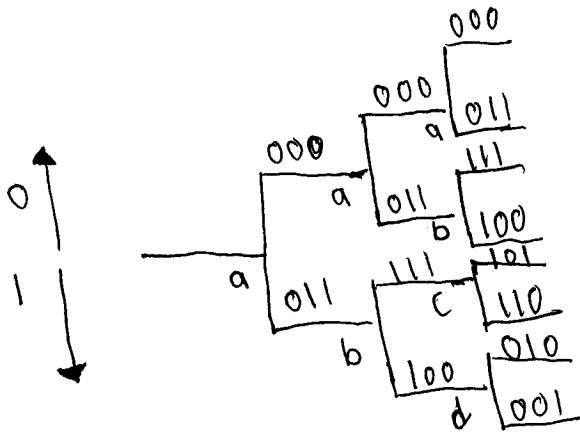
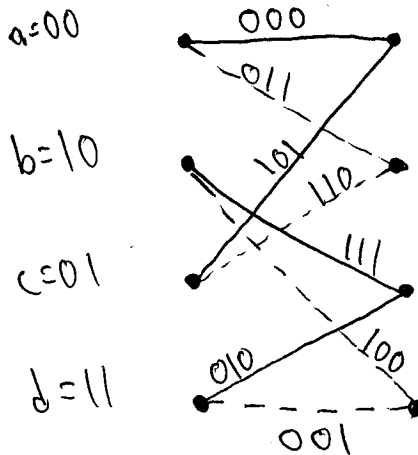
State Diagram



--- input bit 1
 ——— input bit 0

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(6.1 cont'd)

Tree DiagramTrellis Diagram

Problem 2 (6.3 in Sklar)

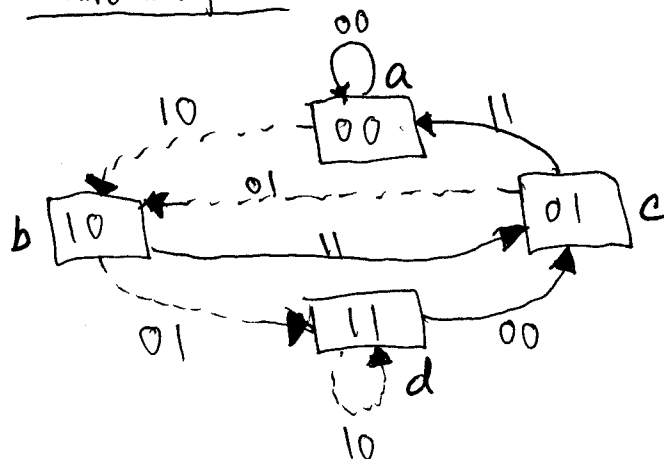
$$K=3 \quad g_1(X) = 1 + X + X^2$$

$$g_2(X) = X + X^2$$

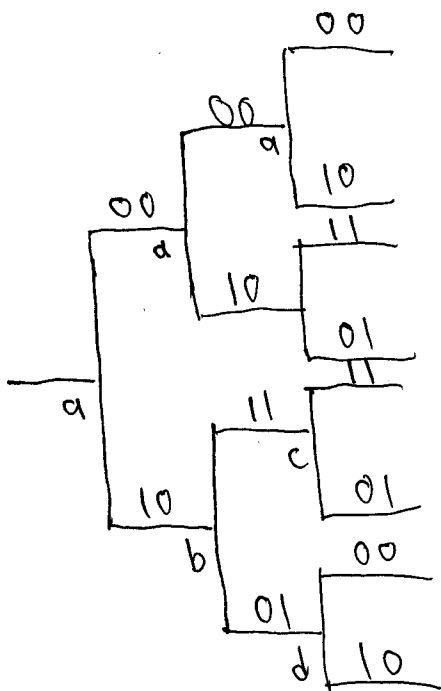
$$\text{rate} = \frac{1}{2}$$

—— input bit 0
 - - - - input bit 1

State Diagram



Tree Diagram



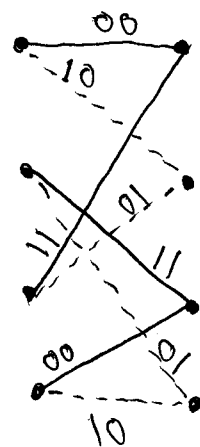
Trellis Diagram

$$a=00$$

$$b=10$$

$$c=01$$

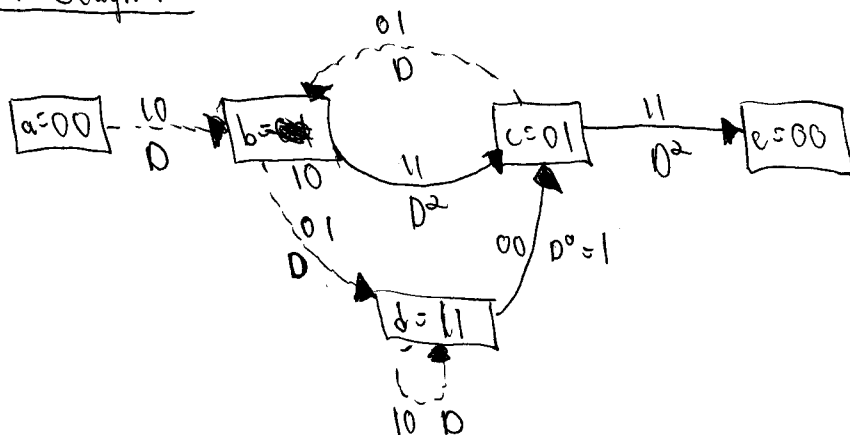
$$d=11$$



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Problem 3 (6.8 in Sklar)

Using the results from Problem 3:
modified state diagram



state equations

$$X_b = DX_a + DX_c$$

$$X_c = D^2 X_b + X_d$$

$$X_d = DX_b + DX_c$$

$$X_e = D^2 X_c$$

$$\text{transfer function } T(D) = \frac{X_e}{X_a} = \frac{D^2 X_c}{X_a} = \frac{D^2 [D^2 X_b + X_d]}{X_a} \\ = \frac{D^4 X_b + D^3 X_d}{1-D}$$

$$X_d = DX_b + DX_c \\ \Rightarrow X_d = \frac{D}{1-D} X_b$$

$$X_b = DX_a + DX_c \\ \Rightarrow X_a = \frac{1}{D} X_b - X_c \\ = \frac{1}{D} X_b - [D^2 X_b + \frac{D}{1-D} X_b]$$

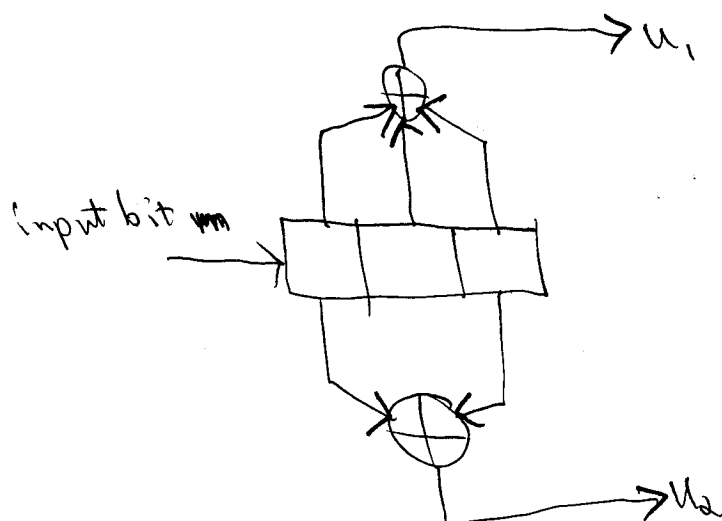
$$= \frac{X_a}{D^4 X_b + \frac{D^3}{1-D} X_b} \\ = \frac{\frac{1}{D} X_b - D^2 X_b - \frac{D}{1-D} X_b}{D^4 X_b + \frac{D^3}{1-D} X_b} \\ = \frac{\frac{1-D}{D} - (D^2 - D^3) - D}{D^4 + \frac{D^3}{1-D}} \\ = \frac{1-D-D^3+D^4-D^2}{D^4+D^5-D^6} \\ T(D) = \frac{1-D-D^3+D^4-D^2}{1-(D+D^2+D^3-D^4)} \\ = D^4 + 2D^5 + 2D^6 + \dots$$

$$s_o, \boxed{\Delta f = 4}$$

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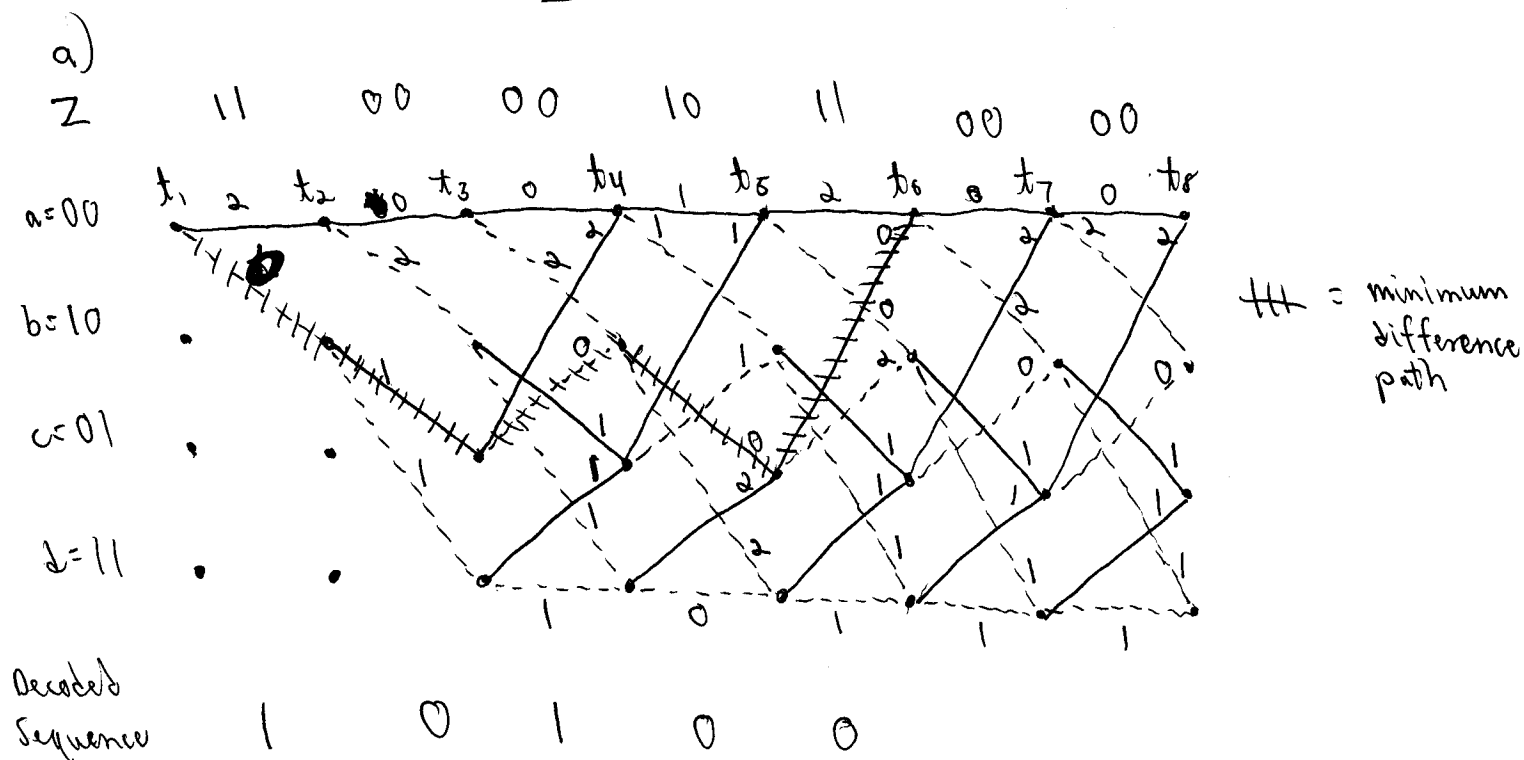
Problem 4 (6.10 in Sklar)

Figure 6.3 encoder ($K=3$, rate = $\frac{1}{2}$)



receive $Z = (1100001011 \text{ rest "0"})$

use the Trellis diagram in Fig 6.7



b) if $m = 10100$ was the ~~message~~ message, then the encoded sequence $U = 1110001011$.
 Since $Z = 1100001011$,
 ↑
 This bit is in error.

18-550 HW#6 SolutionsProblem 5 (6.12 in Sklar)

$$\frac{E_b}{N_0} = 6 \text{ dB} \quad \text{coherent BPSK} \quad \text{rate} = \frac{1}{2} \quad \frac{E_c}{N_0} = r \frac{E_b}{N_0}$$

From Section 6.4.4, for the Figure 6.3 encoder we have

$$\left. \frac{\Delta T(D, N)}{\Delta N} \right|_{N=1} = \frac{D^5}{(1-2D)^2}$$

By equation 6.21,

$$P_B \leq Q\left(\sqrt{2df \frac{E_c}{N_0}}\right) e^{\left(df \frac{E_c}{N_0}\right) \left. \frac{\Delta T(D, N)}{\Delta N} \right|_{N=1, D=e^{-\left(\frac{E_c}{N_0}\right)}}$$

From Section 6.4.1, $df \leq 5$.

$$\frac{E_c}{N_0} = \frac{1}{2}(6 \text{ dB}) = 3 \text{ dB} \approx 2.$$

$$P_B \leq Q\left(\sqrt{2 \times 5 \times 2}\right) e^{(5 \times 2)} \cdot \frac{e^{(-2)^5}}{(1-2 \cdot e^{-2})^2}$$

$$= Q(\sqrt{20}) e^{10} \cdot (8.535 \times 10^{-5})$$

$$= Q(4.47) \times 1.88$$

$$\approx \frac{1}{4.47\sqrt{2\pi}} e^{-10} \times 1.88 = \boxed{7.6 \times 10^{-6}}$$

b) Uncoded:

$$\text{BPSK: } P_B = Q\left(\sqrt{2 \frac{E_b}{N_0}}\right) = Q\left(\sqrt{2 \times 3.98}\right) = Q(2.82).$$

$$\text{From Table B.1, } Q(2.82) = \boxed{2.4 \times 10^{-3}}$$

$$\text{Performance Improvement} = \frac{2.4 \times 10^{-3}}{7.6 \times 10^{-6}} = \boxed{315.8}$$

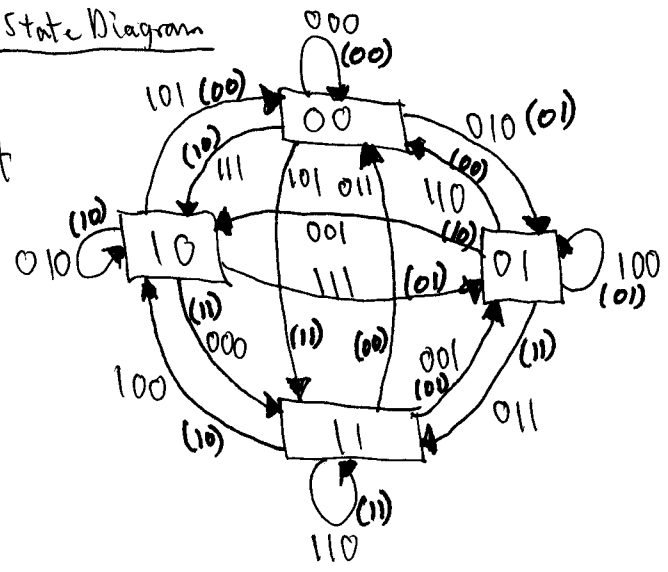
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Problem 6 (6.17 in Sklar)

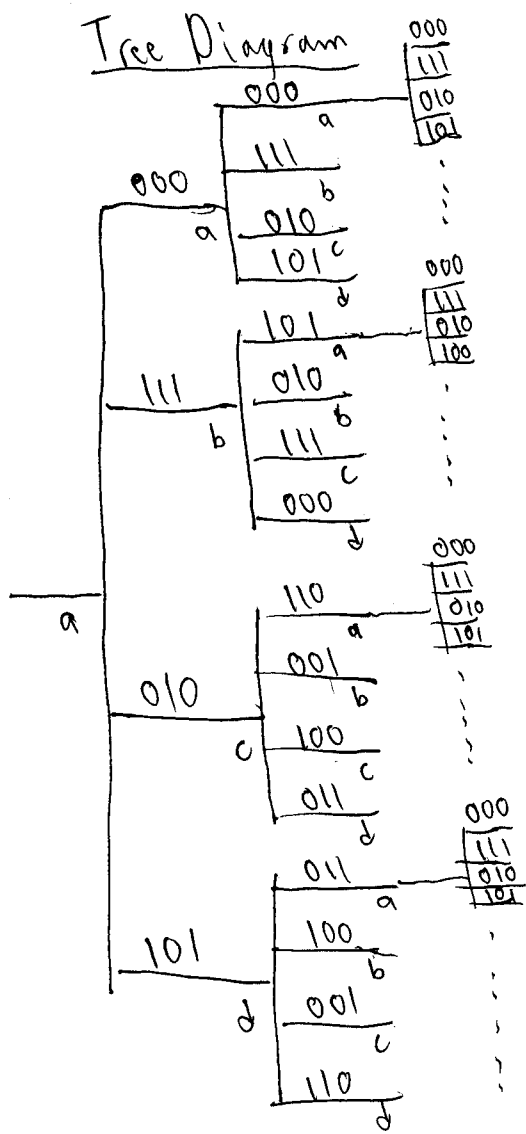
rate = $\frac{2}{3}$ state contents are rightmost
 $k=2$ $K-1=1$ 2-bit registers
 $K=2$

Since 2 input bits come in at a time, there are 4 outgoing branches from each state.
 we show the input in parentheses

State Diagram



Tree Diagram



Trellis Diagram

$a=00$
 $b=10$
 $c=01$
 $d=11$

