

## Permission to use sim-GALS simulator for 18-743 class project

\_\_\_\_\_ is permitted to use the sim-GALS<sup>1</sup> simulator **only** for work related to the 18-743 (Energy Aware Computing) project, until **December 19, 2002**. Any other use is not allowed without the **explicit, written** permission of the authors. This permission expires on **December 19, 2002** or on the date the simulator will be **publicly available**, whichever is earlier.

Student signature

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Faculty signature

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Date

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Related references:

[1] A. Iyer and D. Marculescu, "Power-Performance Evaluation of Globally Asynchronous, Locally Synchronous Processors", in Proc. Intl. Symposium on Computer Architecture (ISCA), Anchorage, AK, May 2002.

\_\_\_\_\_ <sup>1</sup> Sim-GALS is a Globally Asynchronous, Locally Synchronous cycle-accurate microarchitectural simulator described in [1].