(1) Use Case

We intend to make a unique instrument effects pedal, combining some of the functionality of a sequencer with a basic delay pedal.

- Signals
 - Manipulating audio
- Circuits
 - Input and output
 - Parameter dials
- Software
 - Algorithms to modify digital signals



(2) Use Case Requirements

Controls

- Our pedal must be easily controlled on stage.
- Users will prefer conventions that are similar to existing devices.
- Users will expect to turn the effect on or off **instantaneously**.
 - Bypass control should be large and simple enough to be activated by foot.
- The pedal's parameters must be able to be modified in **seconds**.
 - Dials will control delay time
 - Switches will control the sequencer setup
- The pedal's state must be visible.
 - An LED screen will display the tempo in beats per minute.

(2) Use Case Requirements

Size and Shape

- Our pedal must not take up much room on stage.
 - Length and width must be small; less than **8 in** for each dimension.
- Our pedal must be stable, so it will not fall over.
 - A flat base is required.
 - Height must be short (less than **4 in**) and smaller than length and width.
- Our pedal will be rectangular to fit on a board without wasted space.

(2) Use Case Requirements

Latency

- Our pedal must have an unnoticeable wait between the input signal and its corresponding output.
 - Latency of about 10ms is noticeable by performers.
- Users will expect to use our pedal in series with other pedals
 - Our pedal may be one part of a long signal chain.
- Latency of **5ms** or less is necessary so that our pedal can be used in conjunction with other effects.

(3) Technical Challenges

- 1. To perform accurate digital signal processing, need effective analog-to-digital and digital-to analog converters
 - a. Test multiple commercial ADC/DAC
 - b. Track speed, accuracy, and noise on our audio inputs in simulation software
- 2. Sufficient memory storage given sampling rate and desired effects
 - a. Hardware must support calculation-based storage needs
 - b. Current proposed microcontroller *Daisy Seed* chosen given its extensive libraries and memory/audio buffer size
 - i. Contains 64 MB of SDRAM
 - ii. Up to **10 minute** long audio buffers
 - iii. 8MB external flash

(3) Technical Challenges

- 3. Pedal Sizing
 - a. Housing for our circuitry must match industry standards
 - b. Circuitry and other hardware choices must align
- 4. User Interface
 - a. Must be easy and clear to interact with pedal settings
 - b. Incorporating dials and small LCD display to display pedal setting selections

(4) Solution Approach

- Currently plan to use microcontroller to apply the delay effect
 Useful if we want to add additional features in software
- Other basic functionalities and controls done through hardware
- Need to make custom housing for pedal components, as pre-existing ones do not have the required parts for the sequencer functionality
 Will likely design in CAD and 3D print

(4) Solution Approach: Specifics

- Component Requirements:
 - Buttons with visual indicators to control sequence
 - Screen and buttons, perhaps combined, to control and display tempo of delay
 - ADC and DAC, must be higher quality to maintain sound quality, and fast to reduce latency

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(5) Testing, Verification, Metrics - Quantitative

- Develop input data set from live musicians or handpick online samples in the form of .wav files
- Test our implementation using microcontroller software simulations
- Incorporate hardware and utilize oscilloscope to verify desirable output waveforms
 - Measure frequency, amplitude, latency, etc.
- Produce differential waveform between input and output waveforms to determine effectiveness of our design
- Probe for power, performance, and area of circuit design
 Use findings in testing to improve design or explore other avenues

(5) Testing, Verification, Metrics - Qualitative

- The artistic nature of our product demands qualitative testing
 - Run several trials with live audio
 - Measure input and output audio and store in some data format
 - Conduct similar analysis to software simulation to improve the audio quality and the closeness between our desired effect and actual effect
 - Receive feedback from professionals on the resulting sound
 - Ask same set of questions to professionals

(6) Tasks and Division of Labor

Josie

Nick

- Signal
 Design/Processing
- Components
 Research
- Software Design
- Microcontroller
 Programming

- Pedal Housing
- Software Research
- User Interface Design
- Additional Hardware Wiring + Design Choices

Chaitanya

- Hardware Research for circuit-related decisions
- Hardware Design in the form of PCB design to tapeout.
- Microcontroller
 Programming
- Additional Hardware
 Wiring + Design Choices

(7) Schedule

Sequence

PROJECT TIT	TLE	Sequence Delay Pedal	COMPANY NAME	ECE Design Experience - Group D0										
	TASK TITLE		PHASE ONE				PHASE TWO		PHASE THREE			PHASE FOUR		
WBS NUMBER		TASK OWNER	WEEK 1 (1/27)	WEEK 2 (2/3)	WEEK 3 (2/10)	WEEK 4 (2/17)	WEEK 5 (2/24)	WEEK 6 (3/10)	WEEK 7 (3/17)	WEEK 8 (3/24)	WEEK 9 (3/31)	WEEK 10 (4/7)	WEEK 11 (4/14)	WEEK 12 (4/21)
			MTWRF	MTWRF	M T W R F	M T W R F	M T W R F	M T W R F	MTWRF	MTWRF	M T W R F	M T W R F	M T W R F	M T W R
1	Project Research and Ideation	n												
1.1	Brainstorming	All												
1.1.1	Hadware Research	Chaitanya												
1.2	Software Experimentation	Josie												
1.3	Design Planning	Nick												
1.4	Proposal Slides and Prep	All												
	Proposal Presentation	Josie												
1.5	Components Research	All												
2	Design													
2.1	Schematic Design	C/N												
2.2	Microcontroller Research	Josie												
2.3	Software Design	Josie												
2.4	PCB Design	Chaitanya												
2.5	Housing Design	Nick												
2.6	Meeting to Discuss Design	All												
2.7	Slack	All												
3	Parts													
3.1	PCB Printing	Chaitanya												
3.2	3D Printing	Nick												
3.3	Microcontroller Testing	Josie												
3.4	Software Experimentation	All												
3.5	Slack	All												
4	Implementation and Testing													
4.1	Implementation	All												
4.2	Testing	All	- hannal anna hanna hanna da anna d		n de construir de co									
4.3	Final Poster and Video	All												
4.4	Final Report	All												