

SYSTEM BLOCK DIAGRAM	
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1

2

3

4

5

6

A

A

B

B

C

C

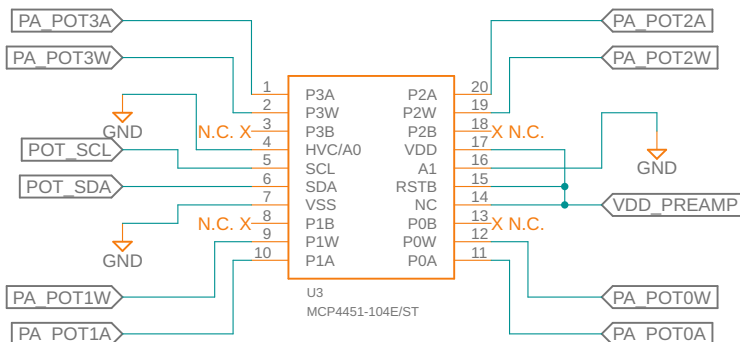
D

D

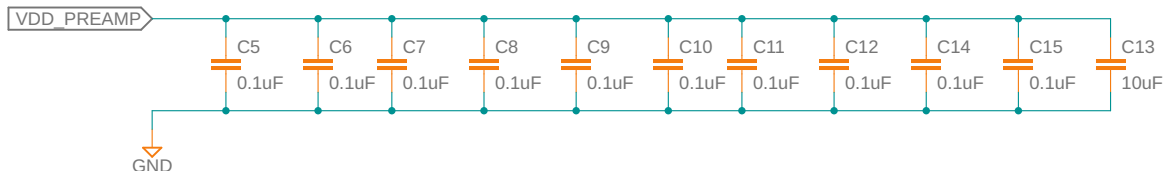
E

E

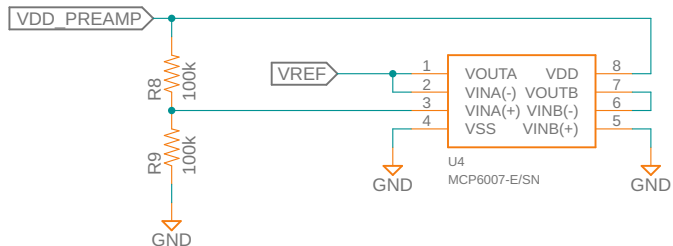
PRE-AMP DIGITAL POTENTIOMETER (QUAD)
ADDR BITS = 00



NOTES:
1. Place one 0.1uF capacitor near each IC.



VREF = LOW-IMPEDANCE VIRTUAL GROUND



PRE-AMPLIFIER (Digipots and Supplies)

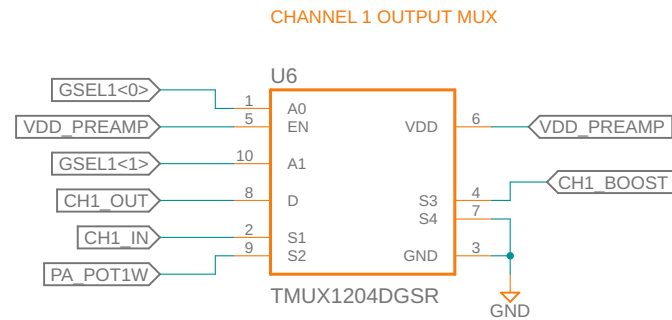
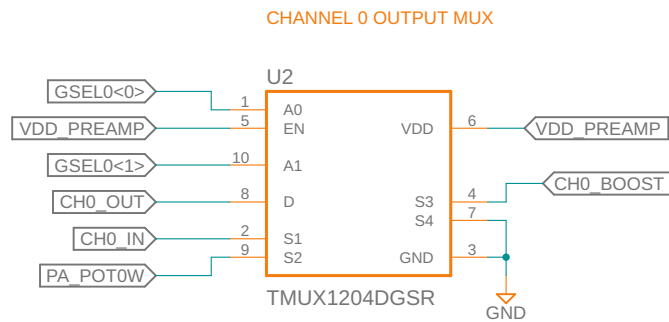
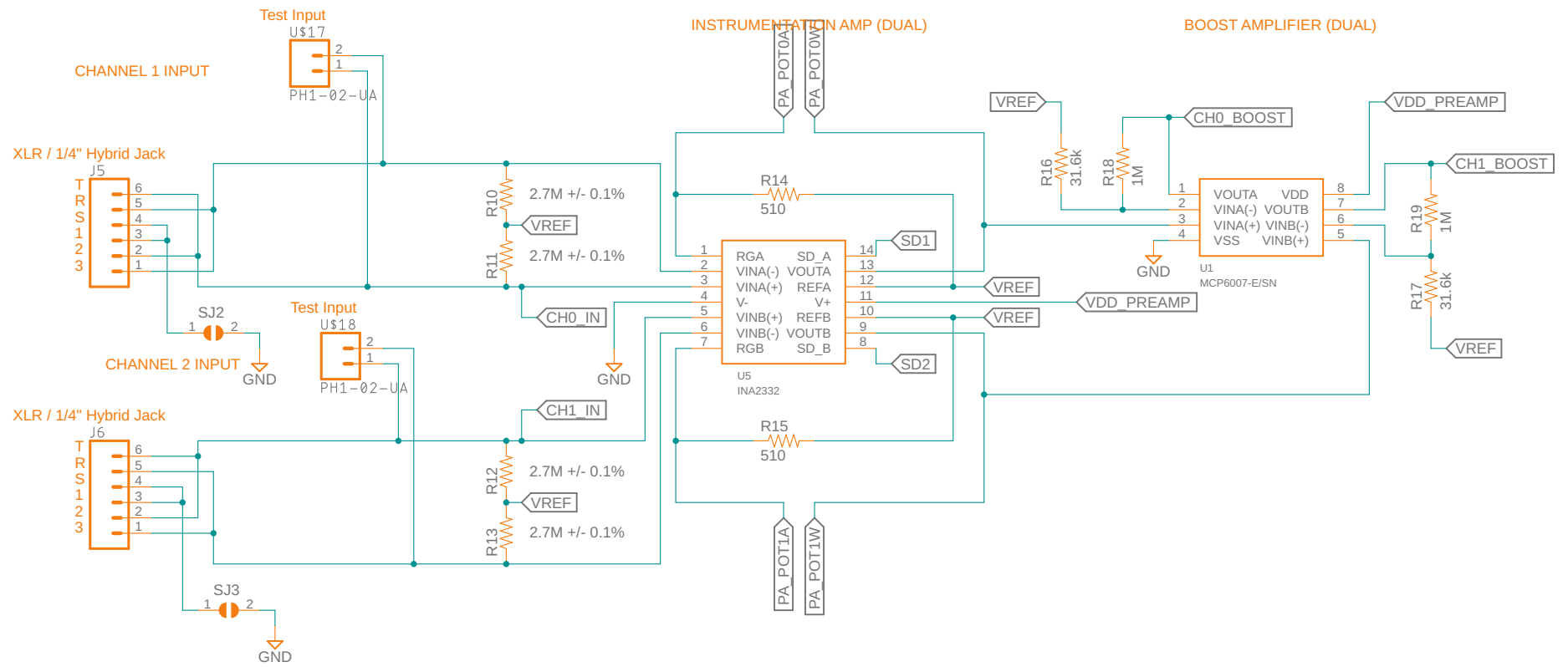
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PRE-AMPLIFIER (Channel 0 and 1)

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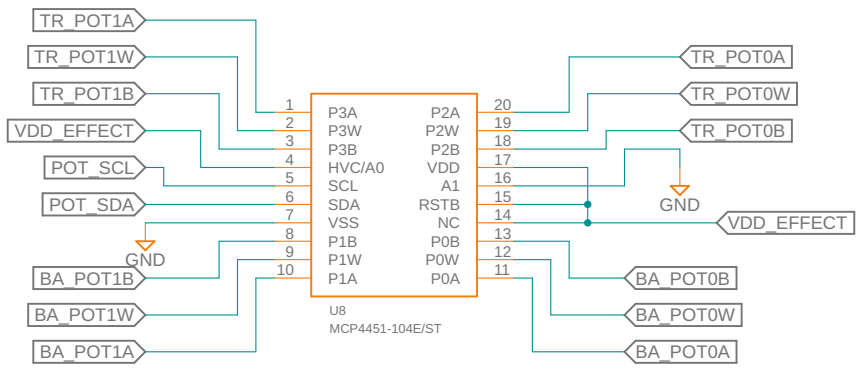
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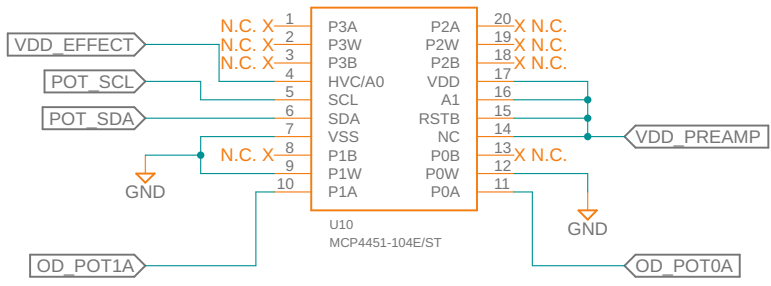
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ANALOG EFFECT BASS/TREBLE POT. FOR CH0-1 (QUAD)
ADDR BITS = 01

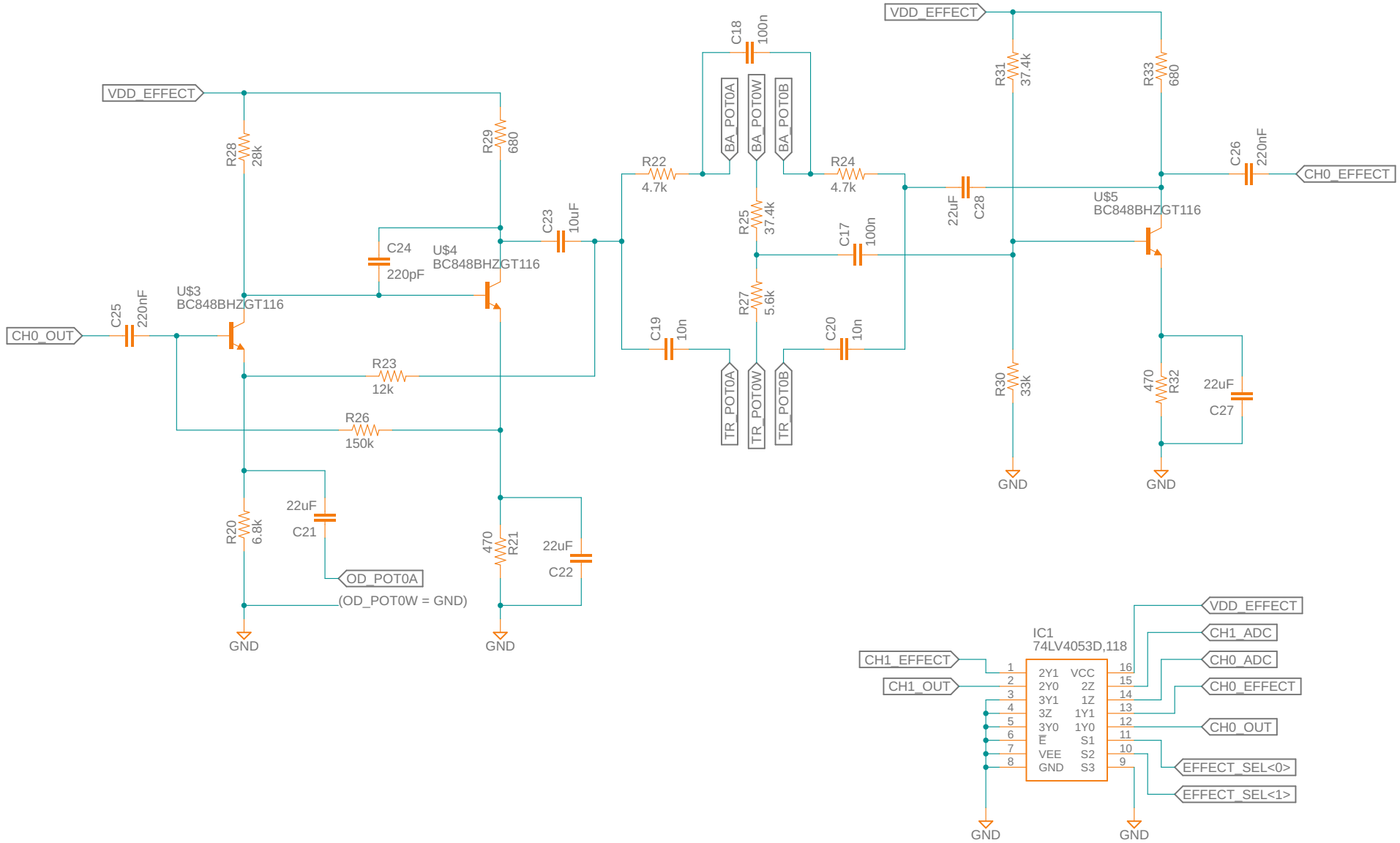


ANALOG EFFECT OVERDRIVE POT. (QUAD)
ADDR BITS = 11



ANALOG EFFECT (Digipots)

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ANALOG EFFECT (Channel 0)

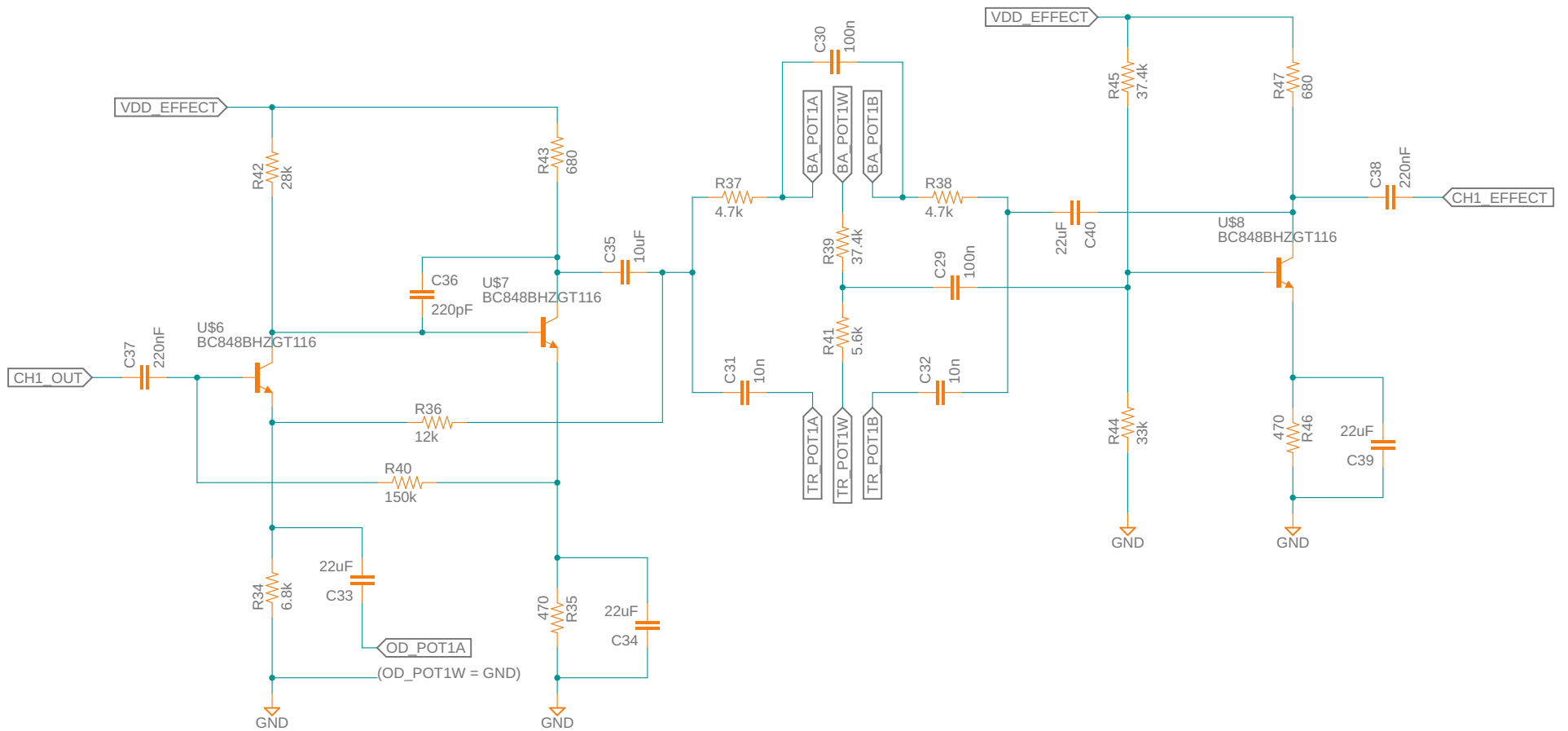
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ANALOG EFFECT (Channel 1)

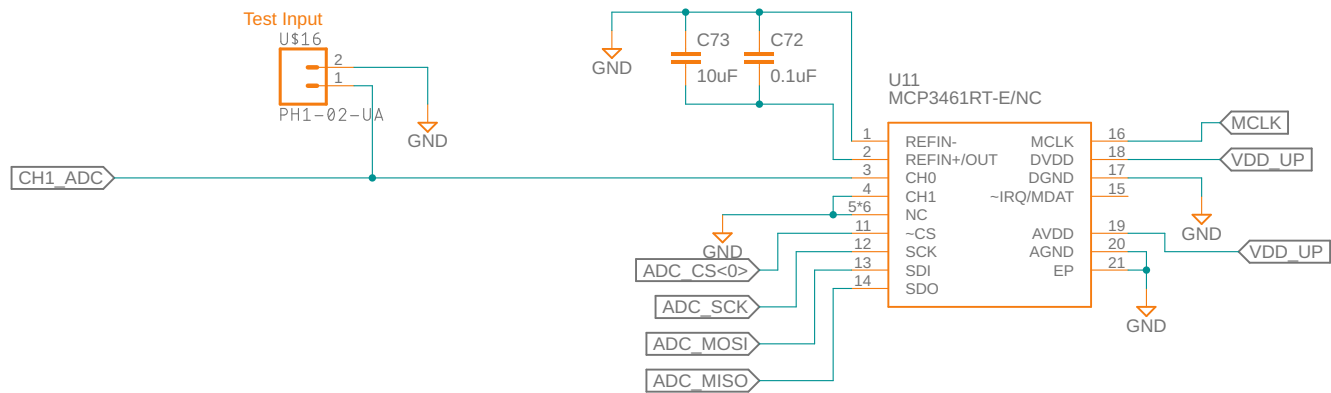
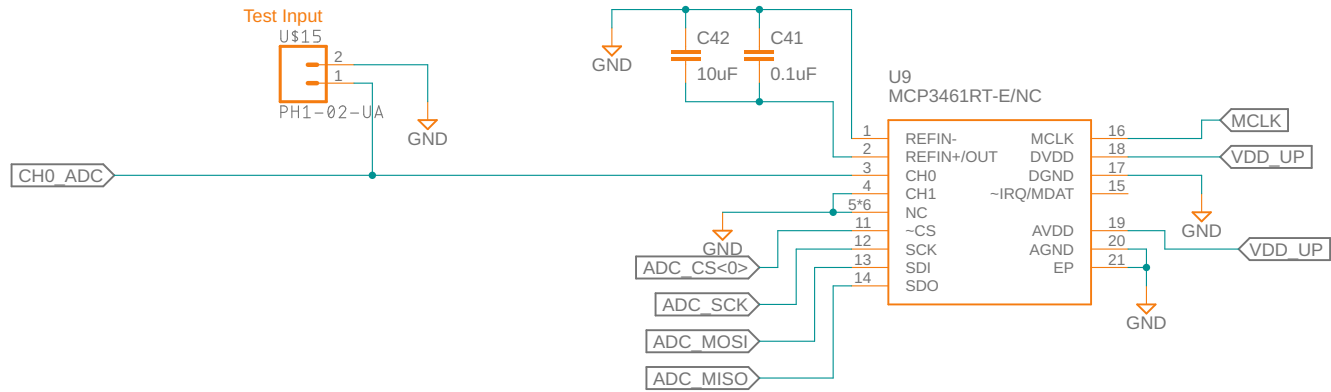
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MICROPROCESSOR (External ADCs)

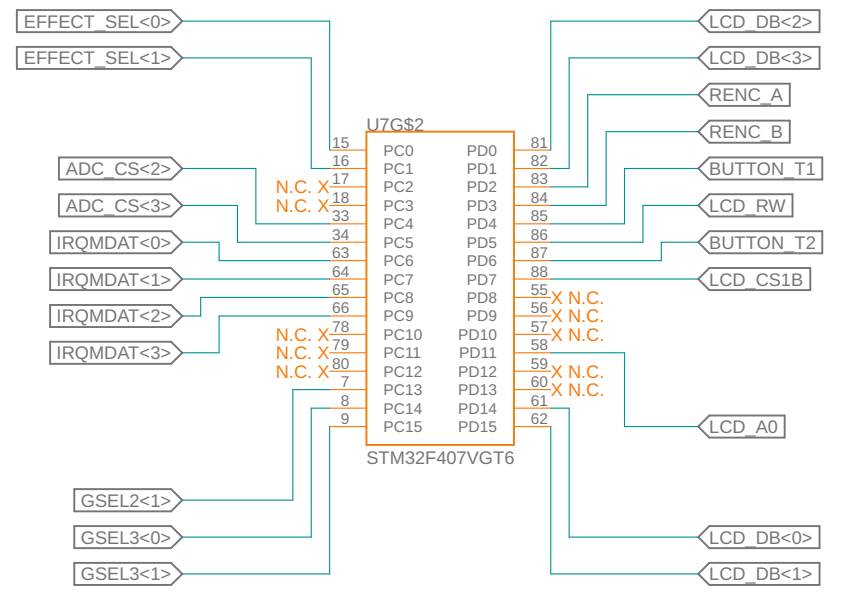
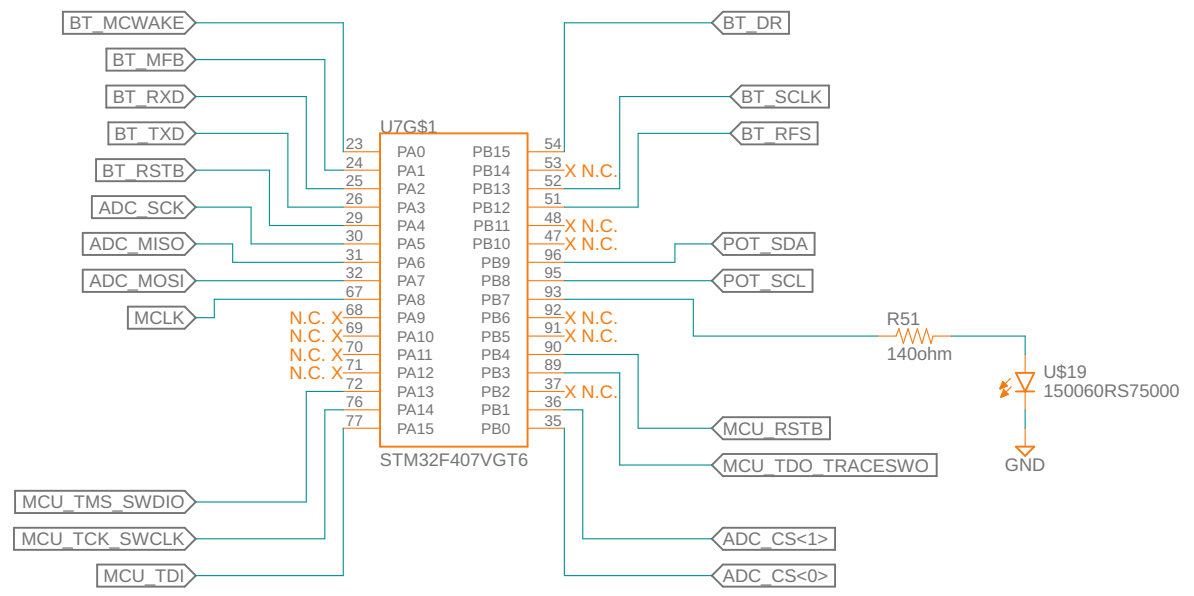
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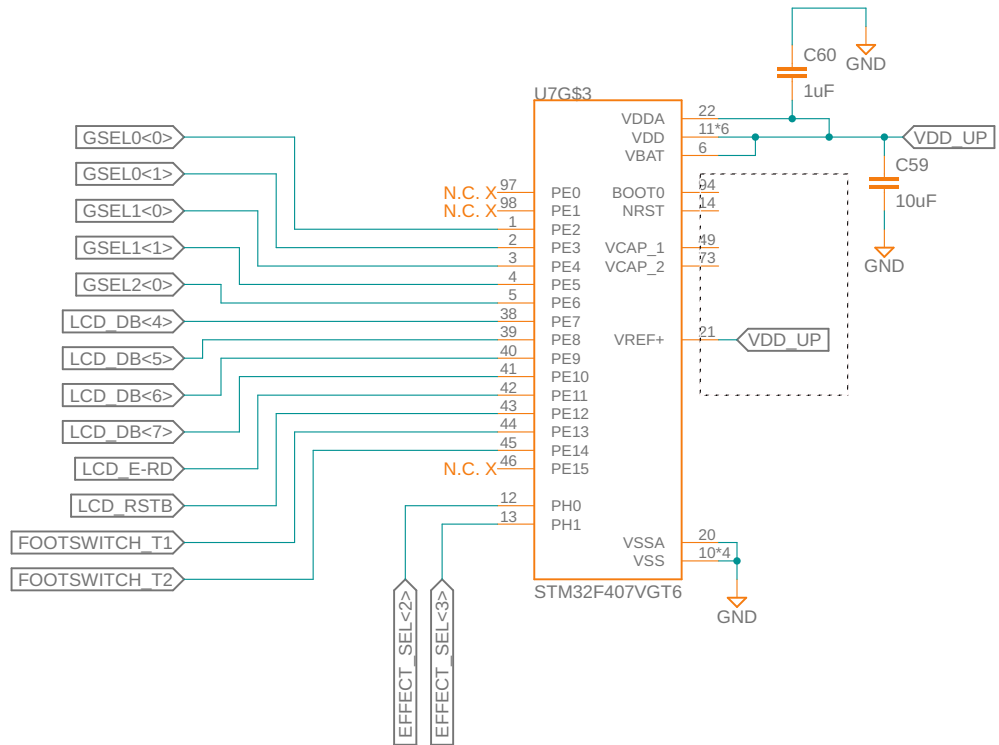
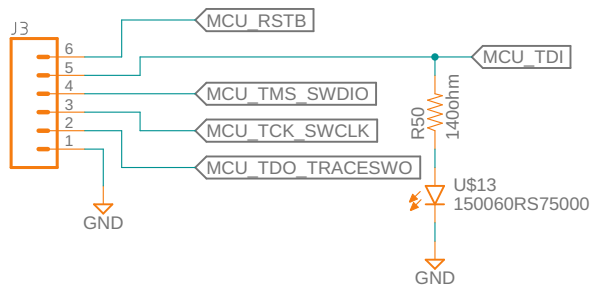
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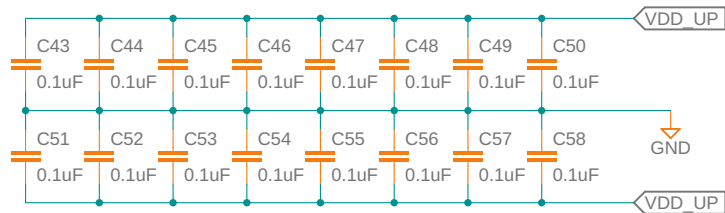
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MICROPROCESSOR: (I/O Ports)	
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NOTE
Each VDD/VSS pair (15 tot.) and VDDA/VSSA should be decoupled with 0.1uF



MICROPROCESSOR (Miscellaneous)

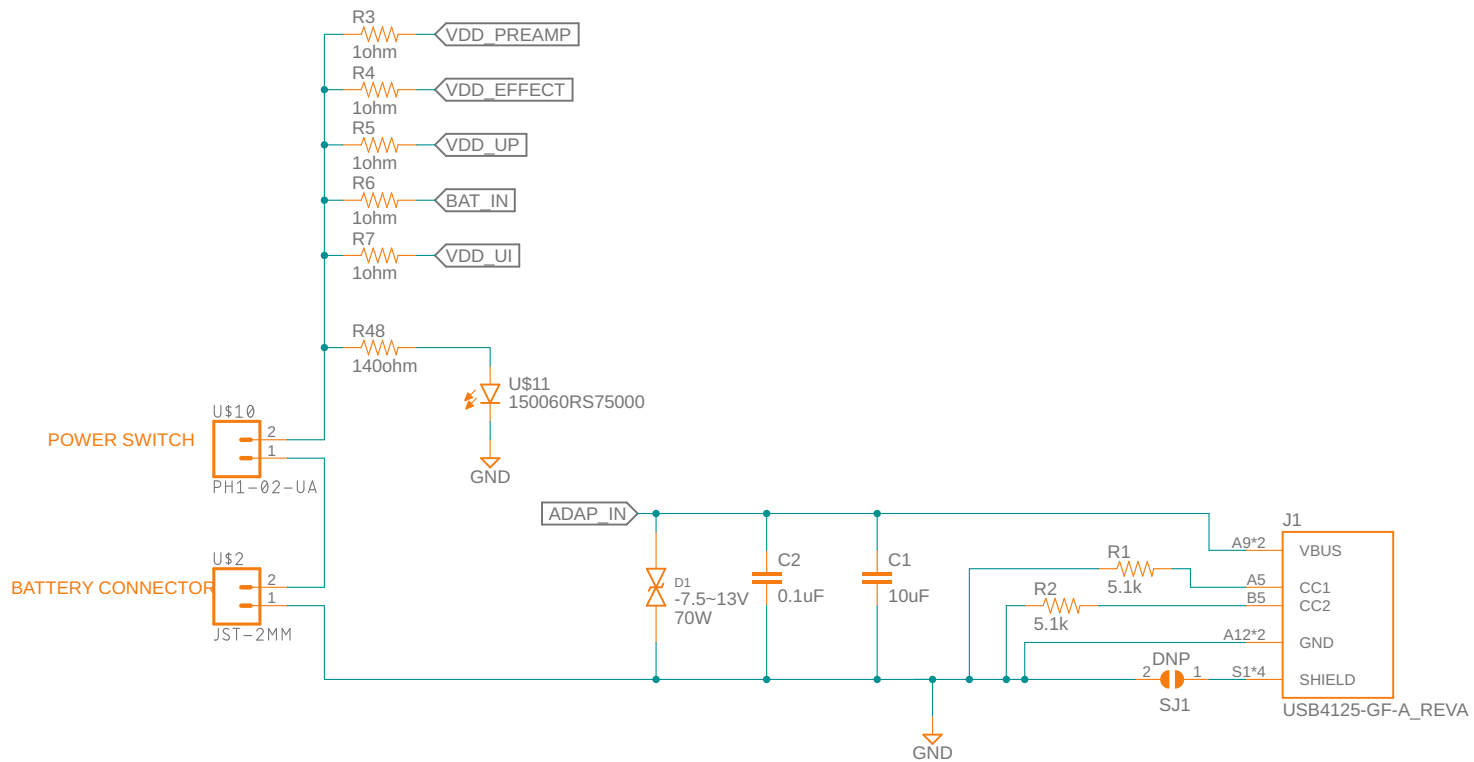
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POWER MODULE

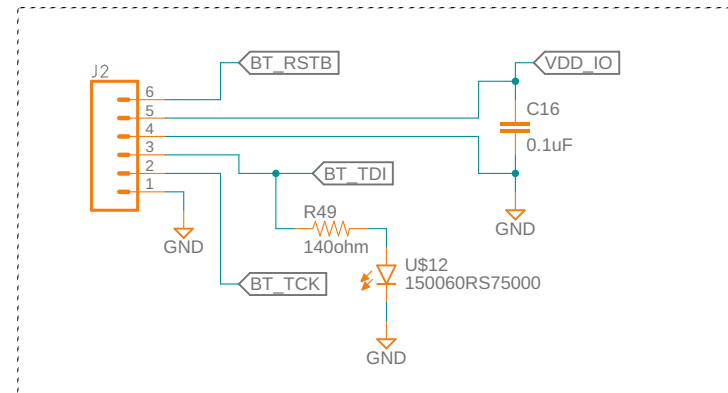
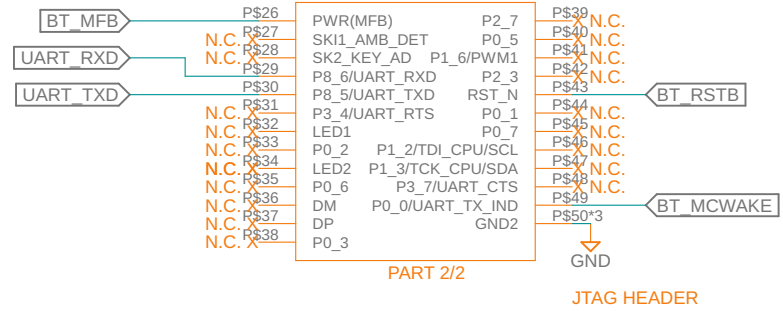
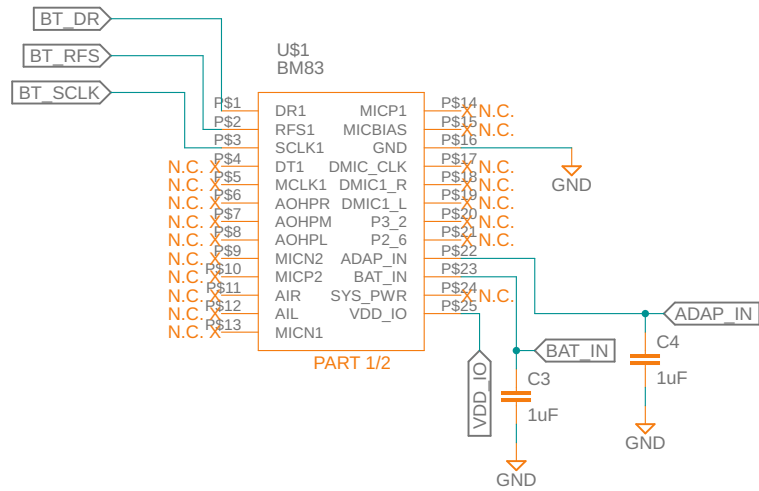
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BLUETOOTH MODULE

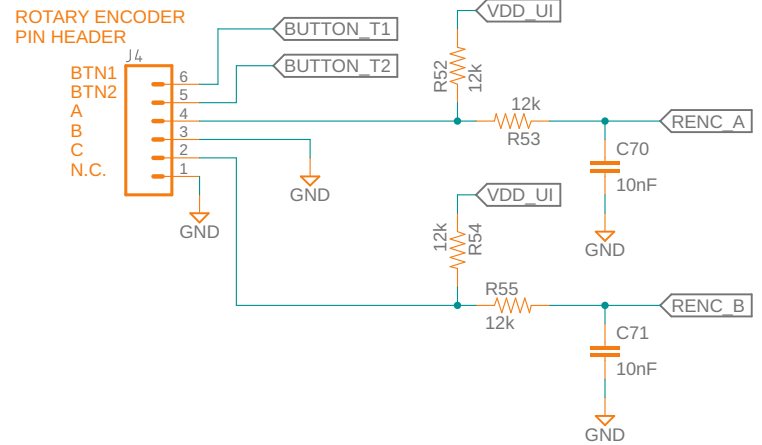
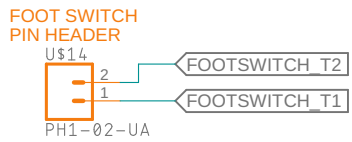
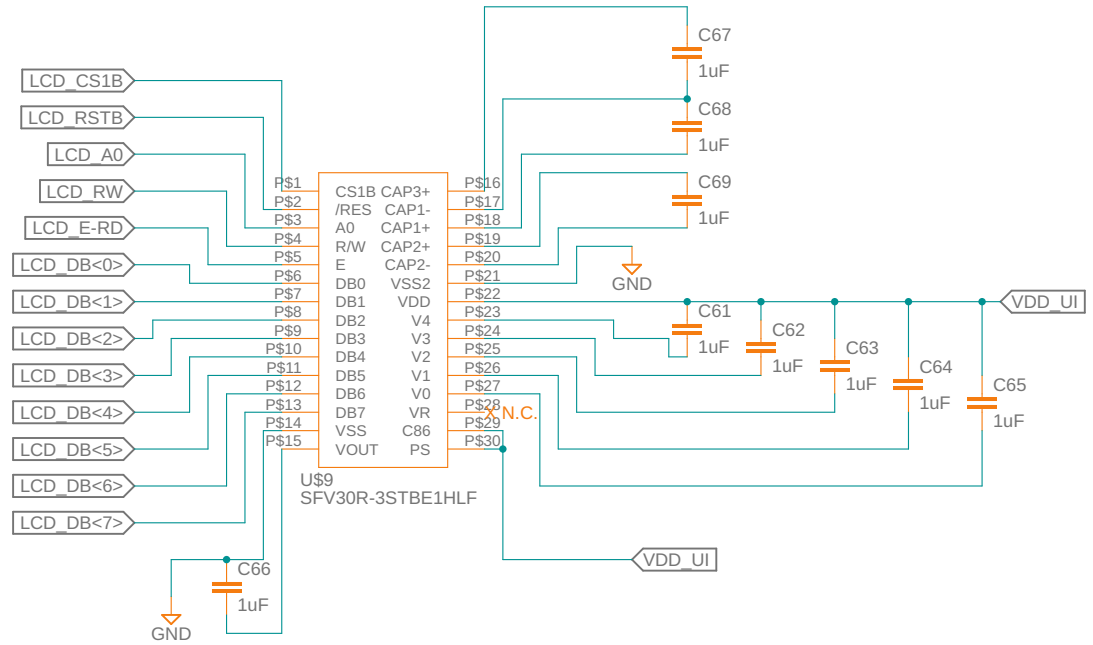
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UI MODULE

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