Shruti Narayan
Team B2

What did you personally accomplish this week on the project? Give files or photos that demonstrate your progress. Prove to the reader that you put sufficient effort into the project over the course of the week (12+ hours).

- This was a busy week for me because my organization had an away competition and I had to travel (noted in the schedule)
- We worked together to figure out how to save 5 sec of video frames into appropriate area in SDRAM and then read back. This was a huge time sink and we made some progress but ended up not finding a solution to the issue (unexpected errors in output). We chose to work with a contingency for demo - we will simulate the SDRAM in a testbench to show that the FPGA part of the algorithm works (the actual seam carving). I will write the testbench on Monday
- I spent a lot of time rewriting the top module to account for our changed design
- I designed the new column penalization and partial sum calculations, then rewrote the implementation of stage 1

Is your progress on schedule or behind? If you are behind, what actions will be taken to catch up to the project schedule?

- Slightly behind due to design changes and issues with storing video in SDRAM
- I will work over the weekend to catch up. We have also adjusted our schedule and what our demo for next week will look like - we will postpone the linkage between HPS and FPGA components to have more time figuring out the camera video input SDRAM issue. For the demo we aim to show the FPGA algorithm part working and I will work to ensure those modules are complete (with design changes implemented) for Monday

What deliverables do you hope to complete in the next week?

- Finish processing portions of all modules complete (I will do energy map ones and others will complete other stages)
- Integrate with each other on Monday, have the full algorithm ready in simulation for Wednesday demo