

Title: TEAM STATUS

Team B2 - Eshani Mishra (emishra), Kimberly Lim (klimjinx), Shruti Narayan (shrutin1)

What are the most significant risks that could jeopardize the success of the project? How are these risks being managed? What contingency plans are ready?

- Pixel indices offset - We realized that pixel indices would change every time the seam carving algorithm is run, and this could cause issues when we are deciding which pixels to load for each run of the algorithm. We have two contingency plans for this issue:
 - Use SoC to actually remove seams from video, and have the size as an adjustable parameter for the algorithm
 - Store SDRAM memory address of each pixel to be “ignored”, so we could know which pixels to skip when reloading the frames for the next run of the algorithm
- Camera monitor resolution - When we changed the resolution of the video, there were errors in displaying the video on the monitor
 - Contingency - Only load the top left 256 * 144 pixels per frame into SDRAM

Were any changes made to the existing design of the system (requirements, block diagram, system spec, etc)? Why was this change necessary, what costs does the change incur, and how will these costs be mitigated going forward?

- Eliminated preprocessing on SoC - all image input comes from the camera and goes directly into FPGA SDRAM

Provide an updated schedule if changes have occurred.

