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Team B2

What did you personally accomplish this week on the project? Give files or photos that demonstrate your progress. Prove to the reader that you put sufficient effort into the project over the course of the week (12+ hours).

- Updated design of stage 1 with changes and revised implementation
- Changed embedded memory format and layout
- Integrated all three stages and put together simulation testbench to get ready for demo
- Spent time (.....hours over days.....) debugging stages of FPGA implementation on waveform viewer. Many minute changes to FSM, handshaking signals, pipeline and alignment of pipeline and data load/storing, addressing, etc
- Discussed algorithm implementation nuances (energy map calculation absolute value issue)
- Worked on final presentation slides (updated block diagrams and timing analysis) and discussed metrics

Is your progress on schedule or behind? If you are behind, what actions will be taken to catch up to the project schedule?

- Slightly behind due to debugging time and issues with video transfer over bridges
- We have set a new clear goal that, with great work everyday, we will see finished
- Have identified extra all-nighters to accomplish work and consulted (or planning to consult) domain experts

What deliverables do you hope to complete in the next week?

- Integrate with HPS side
- Start checking seam pixel correctness metrics against benchmark, look into video quality benchmark
- Variable number of columns in algorithm
- Get final demo ready