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Team B2

What did you personally accomplish this week on the project? Give files or photos that demonstrate your progress. Prove to the reader that you put sufficient effort into the project over the course of the week (12+ hours).

- Writing FPGA module to write into HPS memory through the SDRAM controller
- Writing Stage 3 module input/outputs with Eshani and Shruti

Is your progress on schedule or behind? If you are behind, what actions will be taken to catch up to the project schedule?

- Slightly behind due to design changes and issues with storing video in SDRAM
- We have also adjusted our schedule and what our demo for next week will look like - we will postpone the linkage between HPS and FPGA components to have more time figuring out the camera video input SDRAM issue. For the demo, we aim to show the FPGA algorithm part working and I will work to ensure those modules are complete (with design changes implemented) for Monday

What deliverables do you hope to complete in the next week?

- Test Stage 3
- Test FPGA-HPS module