Kimberly Lim
Team B2

What did you personally accomplish this week on the project? Give files or photos that demonstrate your progress. Prove to the reader that you put sufficient effort into the project over the course of the week (12+ hours).

- Writing FPGA module to write into HPS memory through the SDRAM controller
- Discussed Stage 3 module input/outputs with Eshani and Shruti
- Tried (but failed) to give 144p resolution VGA signal to monitor
  - Contingency: Just block off part of screen

Is your progress on schedule or behind? If you are behind, what actions will be taken to catch up to the project schedule?
- Behind
  - We updated the Gantt schedule to bring everything up
  - Spring Carnival was a no work slot but did some catching-up during that time

What deliverables do you hope to complete in the next week?
- Test FPGA-HPS memory writer module
- Write HPS module to create output video