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Team B2

What did you personally accomplish this week on the project? Give files or photos that demonstrate your progress. Prove to the reader that you put sufficient effort into the project over the course of the week (12+ hours).

- Worked on demo with group: viewable seam, correcting resolution, sending frames from camera to monitor
- Set up ethernet on FPGA with router from Prof Low
- Set up Linux GUI on SoC
- Watched Nios II QSYS tutorial
- Working on learning how to use QSYS to connect Nios II code with verilog

Is your progress on schedule or behind? If you are behind, what actions will be taken to catch up to the project schedule?

- On schedule

What deliverables do you hope to complete in the next week?

- Connect verilog with C code using Qsys