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Team B2

What did you personally accomplish this week on the project? Give files or photos that demonstrate your progress. Prove to the reader that you put sufficient effort into the project over the course of the week (12+ hours).

- Preparing for final demonstration (I am the presenter)
- Finished in-class demo on Wednesday
- Made slides for the final demonstration
- Stage 3 module input/outputs compiled and tested for data pipeline
- Writing the FPGA module to write into HPS memory through the SDRAM controller using dual port FIFO

Is your progress on schedule or behind? If you are behind, what actions will be taken to catch up to the project schedule?
- behind due to design changes and issues with storing video in SDRAM
  - Met up with Mrunmayi on the weekend to discuss demon and iron out issues
- We have also adjusted our schedule and what our presentation for next week will look like - working on HPS and FPGA components and scratched SDRAM D8M camera input for FIFO solution

What deliverables do you hope to complete in the next week?
- Test FPGA-HPS module
- Present demonstration on Wednesday
- Submit presentation slides on Monday
- Submit poster on Tuesday