Title: TEAM STATUS

Team B2 - Eshani Mishra (emishra), Kimberly Lim (klimjinx), Shruti Narayan (shrutin1)

What are the most significant risks that could jeopardize the success of the project? How are these risks being managed? What contingency plans are ready?

- The MegaWizard for creating modules to instantiate embedded memory using the M10K blocks revealed an unexpected problem the max size of a RAM module is only around 64 M10K blocks, and we need at least 75 for our smallest storing banks. The device has 557 total M10K blocks so we have not found the reason for this.
 - Risks managed: We can still go forward with this instead of a single bank of 75 blocks, for instances, we can have two banks of size 32 blocks and store the full frame between them. It will involve a bit of extra code to manage both but should not change the design, just create smaller sections to manage.
 - Contingency: We can change our video size to 256px144p, so that the full size of data we wish to partition on and work with will fit in the given module size restraints. For mid-semester demo, since it is so soon, we will be using this solution.
- We are having difficulty moving files onto the SoC because of ethernet connectivity issues. The device is not registered and since the network is secure, we are getting a sandbox IP address instead of a valid one.
 - Risks Managed: We have contacted ECE Help to have our hardware device registered onto the CMU network
 - Contingency: We are using a personal router. This is what we have to do for the demo, and it involves going to Kimberly's house each time we need to move files, a complication we had to deal with to meet this deadline
- Not necessarily a risk, but a choice we were faced with we decided between different Linux images to use: a terminal console or a GUI displayed on monitor through VGA. The GUI provides interesting extensions (image detection, video display) but we are sticking with the simple and tried terminal console for familiarity and ease for demo

Were any changes made to the existing design of the system (requirements, block diagram, system spec, etc)? Why was this change necessary, what costs does the change incur, and how will these costs be mitigated going forward?

• Video resolution size for demo reduced to 144p (256x144) because of unexpected RAM module sizing issue. In the final project we can simply instantiate multiple instances of this and duplicate our calculations, but for the mid-semester demo we will show the data transfer pipeline with this smaller-scaled version

Provide an updated schedule if changes have occurred.

• No changes, we will meet our mid-semester demo deadline