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Team B2

What did you personally accomplish this week on the project? Give files or photos that demonstrate your progress. Prove to the reader that you put sufficient effort into the project over the course of the week (12+ hours).

- Tested out MegaWizard in Quartus to generate RAM and ROM module files
 - Explored issue of allowed module sizes (can only seem to use max 64 blocks?)
 - Created the modules for bank (storage of the video frames)
- Finished writing the Verilog component for demo and tested
- Worked with other team members to integrate the different components for demo (took the majority of this week's work and time, the results of which will be shown at the demo itself)

Is your progress on schedule or behind? If you are behind, what actions will be taken to catch up to the project schedule?

- We are on track to meet the mid-semester demo deadline, but all of us were a bit busy/sick this week so we're certainly cutting it close

What deliverables do you hope to complete in the next week?

- Add the processing components for the stage 1 of the verilog module and test
- Split up the remaining two stage processing work (less complicated) and plan the integration into the final FSM