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Team B2

What did you personally accomplish this week on the project? Give files or photos that demonstrate your progress. Prove to the reader that you put sufficient effort into the project over the course of the week (12+ hours).

- Figured out the *file transfer* issue we had due to the network on the FPGA and our laptop network not being the same
 - We went to my house and connected the FPGA to the WiFi router with an ethernet cable
 - We then used the WiFi signal to send files to FPGA using scp
- Discovered you can put C++ on SoC but need to modify BSP and add external RAM during configuration
<https://vhdlguide.readthedocs.io/en/latest/vhdl/niosoverview.html#create-application-using-c-c>

Is your progress on schedule or behind? If you are behind, what actions will be taken to catch up to the project schedule?

- We are on time

What deliverables do you hope to complete in the next week?

- Present demonstration on Monday
 - Demonstration is the video moving through the entire pipeline but without any computation on the pixels
 - The FPGA will output pixel indices and use those indices to update the video in the ARM processor
- Put script on SoC