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Team B2

What did you personally accomplish this week on the project? Give files or photos that demonstrate your progress. Prove to the reader that you put sufficient effort into the project over the course of the week (12+ hours).

- Wrote out the top module, stage 1 FSMS for handshaking between processing and loading, and dummy modules for the other stages in verilog
- (actual computation not done, just appropriate FSMs and data transfer of video)

Is your progress on schedule or behind? If you are behind, what actions will be taken to catch up to the project schedule?

- We are working accordingly with the slack time - our testing of the modules may not fit in the allotted time but we can use
- Slightly slow week because return from spring break got unexpectedly delayed, but still managed to be on time with schedule

What deliverables do you hope to complete in the next week?

- Finished rough version of algorithm in verilog, with certain modules written as dummies, as well as test modules for all
- Join this with the HPS and have a rough example of the Mid Semester demo, ready to be tweaked and sorted out for two weeks from now