

Eshani Mishra  
Team B2

What did you personally accomplish this week on the project? Give files or photos that demonstrate your progress. Prove to the reader that you put sufficient effort into the project over the course of the week (12+ hours).

- Installed SoC EDA, Quartus Lite, and video metrics software on Windows
- Read through Github tutorials for interfacing between FPGA and HPS on Cyclone 5 boards - <https://github.com/robertofem/CycloneVSoC-examples>
- Worked on FPGA SDRAM tutorial with Nios II
- Worked on DE10 tutorial for controlling FPGA pins through HPS to learn how to use HPS to FPG AXI bridge
- Read through HPS memory controller system description - [https://www.intel.com/content/dam/www/programmable/us/en/pdfs/literature/hb/external-memory/emi\\_fd\\_controller\\_hps.pdf](https://www.intel.com/content/dam/www/programmable/us/en/pdfs/literature/hb/external-memory/emi_fd_controller_hps.pdf)

Is your progress on schedule or behind? If you are behind, what actions will be taken to catch up to the project schedule?

- Behind on writing algorithm - we wanted to focus on getting the video through our data pipeline for our in class demo without processing the video
- We use our built in slack time

What deliverables do you hope to complete in the next week?

- Instantiate SoC module
- Serial transmission of video onto FPGA
- Complete Nios II tutorial