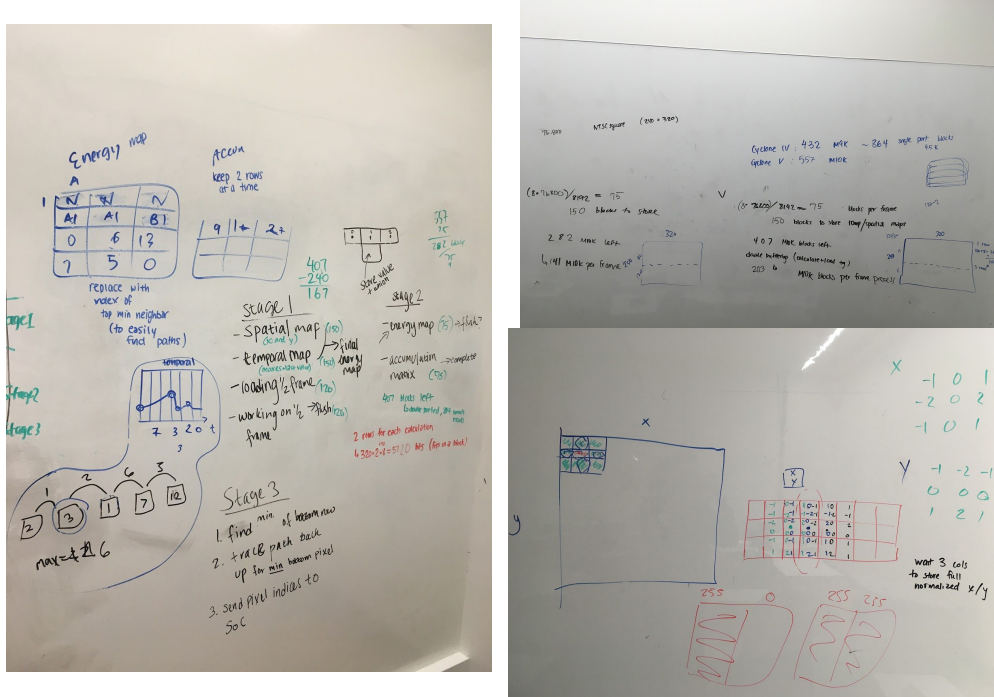


What are the most significant risks that could jeopardize the success of the project? How are these risks being managed? What contingency plans are ready?

- Learning how to use the SOC and dealing with a board different to the one we are used to
 - We are planning to continue work with other tutorials
 - Contingency:
 - We plan to integrate previous projects and consult experts
 - Using DE-10 standard system builder on windows to generate qpf files
- Resource allocation in FPGA
 - We are using Quartus to analyze if our projected estimation match the reality
 - Contingency:
 - Blocking the video into smaller sections using the SoC

Were any changes made to the existing design of the system (requirements, block diagram, system spec, etc)? Why was this change necessary, what costs does the change incur, and how will these costs be mitigated going forward?

- We made changes to the memory utilization for each stage of the algorithm



- We have further considered the design in terms of testing quality
 - Video quality metrics can include user poll, etc

Provide an updated schedule if changes have occurred.

Currently ongoing and future tasks before spring break:

