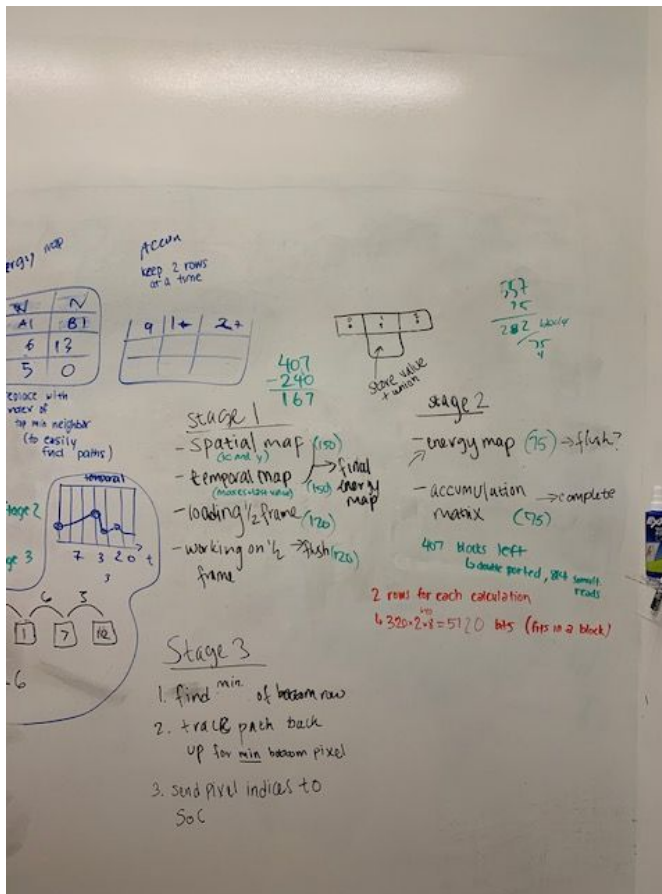


Eshani Mishra  
Team B2

What did you personally accomplish this week on the project? Give files or photos that demonstrate your progress. Prove to the reader that you put sufficient effort into the project over the course of the week (12+ hours).

- Worked with Shruti on algorithm flow on FPGA and task division between FPGA and processor



- Read DE-10 user manual
  - [http://www.terasic.com.tw/cgi-bin/page/archive\\_download.pl?Language=Taiwan&No=1081&FID=551f9fbfa8ed07843cd51831db1b04dd](http://www.terasic.com.tw/cgi-bin/page/archive_download.pl?Language=Taiwan&No=1081&FID=551f9fbfa8ed07843cd51831db1b04dd)
- Worked with Shruti on DE-10 introductory tutorials
- Researched video quality metrics for testing
  - Structural similarity
  - Visual Information Fidelity
  - Signal to noise
- Set up group website and updated Gantt chart with new tasks for group
- Worked on design presentation slides: algorithm implementation, schedule, testing

Is your progress on schedule or behind? If you are behind, what actions will be taken to catch up to the project schedule?

- We are behind on testing the GPIO camera with the DE-10 board since we were hoping for it to arrive this week but it hasn't come in yet
  - We have made the appropriate changes to our schedule to make time to test it next week
- We also made some changes to our design for memory allocation which required additional time but was within our built in design slack time and we have planned out how our FPGA modules will handle memory and the different stages of the algorithm

What deliverables do you hope to complete in the next week?

- Test GPIO camera
- Complete DE-10 tutorials
- Design user poll for testing
- Run quartus evaluation of block memory allocation on test matrices