LOG: 4/7/19 –4/13/19

       I was unable to get as much done this week due to being out of town for a couple of days. Regardless, I have my FPU decoder and execution stages mostly ready to go. I need to make decisions on how to handle a few of the RISC-V F extension instructions (FCLASS, FSGNJ) since they don't rely on the typical FPU operations. The execution units (multiplier, adder, ex.) in the FPU no longer handle rounding. The writeback stage will select which of the execution outputs to write out, and the rounding will happer after that if necessary. This will allow us to use only one rounding unit. We should have integration going by Wednesday (or Tuesday).