

LOG: 3/24/19 –3/30/19

I've been wrapping up the FPU. I fixed the rounding module because of a slight misconception with the round up and round down modes. The accuracy is still about 97% but the few cases where the rounding was causing a problem was resolved. I started to work with david on integrating the FPU into the RISC-V core. I added two modules to convert an int to an float and vice versa. The conversion will represent the integer value in a float format so that the FPU can use it. Other than that, I am just discussing the characteristics of the FPU so that David can easily add it to the core. We hope to have this all working in time for the demo.