

Status Report #2

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Accomplishments

This week, I focused primarily on familiarizing myself with the build infrastructure of GCC, pouring over manuals and reading tangentially related stackOverflow questions in an attempt to understand how the assembler/compiler extension would work to get GCC to take a C program and spit out vector instructions that we can use on the core that we are designing.

These endeavors also included learning about git submodules; and editing the dependencies. I ended up with attempting to edit 4 different repository forks of FOSS, and I will need to spend some time cleaning up and understanding the changes that I have implemented to support our project.

And thirdly, we met with Ken Mai again this week; and know that we need to think more about the applications of our project. We will make sure to think about that going forward.

Schedule

I do believe that we are on schedule; I hope that I will be able to have a proven toolchain to compile the vector instructions that has been tested on a verified RISC-V core before needing to use the tests to validate the hardware that my team is writing. I hope to have at the very least have a version of GCC compiled with the necessary subcomponents to generate vector instructions if not a working test run.