

LOG: 2/24/19 -3/2/19

This week we spent time on the design presentation. This design presentation did not go well. We realized from the presentation that we really need to sit down and figure out the direction for this project. We met with each other as well as with the TA to better understand how to move forward with our project. We hope to meet with Ken Mai to further discuss the situation.

I started to rigorously test the FPU. I slowly went through each operation and made sure that they worked as specified. I generated random tests using a python function. For this past week, I was only able to get through addition/subtraction and multiplication. I was also able to implement a rounding function that allows up to four different rounding modes.

For next week, I would like to have everything floating point wrapped up. This should allow me to help David with the RISC-V core as well as anything that Alex needs me to do. We will meet on Sunday to further discuss the Design document that is due on Monday.